

## EAST SEARCH

7/11/2008

L#	Hits	Search String	Databases
S17	129	S6 and ((hardware or resource) with type)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S1	25105	(processor or computer or "logic unit" or logic) with (parallel or concurrent) with (processing or	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S31	215	S6 and (((hardware or resource)) with (time or occupancy or utilization))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S42	288	S40 or S41	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S25	54	S6 and (deadlock or deadlocked or deadlocking)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S8	151	S6 and ((allocate or allocated or allocating or allocation or assign or assigned or assigning or ε	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S7	73	S6 and ((request or requested or requesting) with (hardware or resource))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
		S6 and ((repeat or repeated or repeating or repeatedly or recursive or recursively) with	
S16	66	(control or controlled or controlling))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S21	27	S6 and (resource with (manager or controller))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S20	63	S6 and (thread with (manager or controller))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S6	554	S2 and S5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S36	2	S6 and ((compare or compared or comparing or comparison) with simulation with (result or out	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S3	30472	(processor or computer or "logic unit" or logic) with (parallel or concurrent) with (processing or	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S15	89	S6 and ((repeat or repeated or repeating or repeatedly or recursive or recursively) with (requer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S38	149	S6 and ((compare or compared or comparing or comparison) with (result or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S10	76	S6 and ((allocate or allocated or allocating or allocation or assign or assigned or assigning or ε	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S18	50	S6 and ((hardware or resource) with (dependency or dependent))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S29	15	S6 and ((detect or detected or detecting or detection or identify or identified or identifying or idε	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S28	4	S6 and ((competition or competing) with (write or read or input or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S33	2	S6 and (thread with (processing or execution or operation) with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S5	4714	S4 and (simulate or simulated or simulating or simulation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S34	5	S6 and (thread with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S22	247	S6 and ((lower or higher) with (hierarchy or level))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S11	20	S6 and ((allocate or allocated or allocating or allocation or assign or assigned or assigning or ε	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S26	2	S6 and ((monitor or monitored or monitoring) with (write or read or input or output) with reques	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S41	269	S39 and S40	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S13	114	S6 and ((parallel or concurrent) with thread)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S14	72	S6 and ((control or controlled or controlling) with thread with (processing or execution or opera	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S24	1	S6 and (request with deadlock)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S32	123	S6 and (thread with (processing or execution or operation) with time)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S23	20	S6 and ((monitor or monitored or monitoring) with request)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S27	150	S6 and ((monitor or monitored or monitoring) with (write or read or input or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S4	34387	S2 or S3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S39	472	S8 or S13 or S15 or S17 or S19 or S22 or S27 or S31 or S32 or S38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S30	2	S6 and ((budget or budgetted or budgeting) with (time or occupancy or utilization))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S35	7	S6 and ((processing or execution or operation) with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S40	288	S7 or S9 or S10 or S11 or S12 or S14 or S16 or S18 or S20 or S21 or S23 or S24 or S25 or S	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S2	4757	(multi-threaded or multithreaded or ((multiple or plurality) near2 (thread or threaded))) with (prc	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S19	208	S6 and ((hardware or resource) with (hierarchy or hierarchical or hierarchized or hierarchize or	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S12	43	S6 and ((sequential or serial) with thread)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S37	2	S6 and ((compare or compared or comparing or comparison) with (estimated or expected) with	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB



S90	73	S89 and ((request or requested or requesting) with (hardware or resource))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S93	20	S89 and ((allocate or allocated or allocating or allocation or assign or assigned or assigning or	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S92	76	S89 and ((allocate or allocated or allocating or allocation or assign or assigned or assigning or	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S95	72	S89 and ((control or controlled or controlling) with thread with (processing or execution or oper	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S94	43	S89 and ((sequential or serial) with thread)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S98	63	S89 and (thread with (manager or controller))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S96	66	S89 and ((repeat or repeated or repeating or repeatedly or recursive or recursively) with (contr	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S97	50	S89 and ((hardware or resource) with (dependency or dependent))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S100	20	S89 and ((monitor or monitored or monitoring) with request)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S99	27	S89 and (resource with (manager or controller))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S103	2	S89 and ((monitor or monitored or monitoring) with (write or read or input or output) with requ	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S101	1	S89 and (request with deadlock)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S102	54	S89 and (deadlock or deadlocked or deadlocking)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S106	2	S89 and ((budget or budgeted or budgeting) with (time or occupancy or utilization))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S104	4	S89 and ((competition or competing) with (write or read or input or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S108	5	S89 and (thread with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S105	15	S89 and ((detect or detected or detecting or detection or identify or identified or identifying or i	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S110	2	S89 and ((compare or compared or comparing or comparison) with simulation with (result or o	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S107	2	S89 and (thread with (processing or execution or operation) with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S109	7	S89 and ((processing or execution or operation) with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S112	152	S89 and ((allocate or allocated or allocating or allocation or assign or assigned or assigning or	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S111	2	S89 and ((compare or compared or comparing or comparison) with (estimated or expected) wi	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S113	114	S89 and ((parallel or concurrent) with thread)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S114	89	S89 and ((repeat or repeated or repeating or repeatedly or recursive or recursively) with (requ	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S117	248	S89 and ((lower or higher) with (hierarchy or level))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S115	129	S89 and ((hardware or resource) with type)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S116	209	S89 and ((hardware or resource) with (hierarchy or hierarchical or hierarchized or hierarchize	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S120	123	S89 and (thread with (processing or execution or operation) with time)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S118	150	S89 and ((monitor or monitored or monitoring) with (write or read or input or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S122	474	S112 or S113 or S114 or S115 or S116 or S117 or S118 or S119 or S120 or S121	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S119	217	S89 and (((hardware or resource)) with (time or occupancy or utilization))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S123	288	S90 or S91 or S92 or S93 or S94 or S95 or S96 or S97 or S98 or S99 or S100 or S101 or S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S121	150	S89 and ((compare or compared or comparing or comparison) with (result or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S125	288	S123 or S124	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S124	269	S122 and S123	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S127	6	S125 and S126	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S126	17	S89 and ((detect or detected or detecting or detection or identify or identified or identifying or i	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S128	299	S123 or S126	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S129	276	S122 and S128	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S130	299	S128 or S129	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S131	17	S130 and S126	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

09/964591 Akio Matsuda et al.

## EAST SEARCH

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### Results of search set S115

Document Kind Codes Title	Issue Date	Current OR	Abstract
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US 20080005112 A1	Predictive log synchronization	20080103 707/8
US 20070300227 A1	MANAGING EXECUTION OF MIXED WORKLOADS IN A SIMULTANEOUS MULTI-THREAD	20071227 718/102
US 20070296729 A1	Unified virtual addressed register file	20071227 345/559
US 20070271556 A1	BUILDING A WAVECACHE	20071122 717/151
US 20070234284 A1	System and method for leveraging independent innovation in entertainment content and graph	20071004 717/109
US 20070234091 A1	Multithreaded dynamic voltage-frequency scaling microprocessor	20071004 713/322
US 20070234070 A1	Software self-defense systems and methods	20071004 713/190
US 20070234016 A1	Method and system for trace generation using memory index hashing	20071004 712/227
US 20070220517 A1	Scheduling in a multicore processor	20070920 718/102
US 20070220294 A1	Managing power consumption in a multicore processor	20070920 713/320
US 20070220232 A1	Data Processing Architectures	20070920 712/20
US 20070219771 A1	Branching and Behavioral Partitioning for a VLIW Processor	20070920 703/15
US 20070217453 A1	Data Processing Architectures	20070920 370/503
US 20070214343 A1	ACROSS-THREAD OUT-OF-ORDER INSTRUCTION DISPATCH IN A MULTITHREADED MI	20070913 712/220
US 20070204139 A1	Compact linked-list-based multi-threaded instruction graduation buffer	20070830 712/218
US 20070204137 A1	Multi-threading processors, integrated circuit devices, systems, and processes of operation an	20070830 712/214
US 20070186208 A1	Mask-Pattern Determination Using Topology Types	20070809 716/21
US 20070186028 A2	SYNCHRONIZED STORAGE PROVIDING MULTIPLE SYNCHRONIZATION SEMANTICS	20070809 711/1
US 20070184369 A1	Lithography Verification Using Guard Bands	20070809 430/30
US 20070174593 A1	Managing and enhancing execution over parallel pipelines	20070726 712/220
US 20070167751 A1	Method and apparatus for vessel characterization	20070719 600/437
US 20070162774 A1	Queued locks using monitor-memory wait	20070712 713/300
US 20070162410 A1	SYSTEMS, METHODS AND APPARATUS FOR AUTOMATA LEARNING IN GENERATION C	20070712 706/48
US 20070136374 A1	METHOD AND SYSTEM FOR PROVIDING ON-LINE INTERACTIVITY OVER A SERVER-CL	20070614 707/104.1
US 20070129924 A1	Partitioning of tasks for execution by a VLIW hardware acceleration system	20070607 703/14
US 20070118596 A1	SYSTEM AND METHOD FOR IMPLEMENTING A CLIENT SIDE HTTP STACK	20070524 709/203
US 20070115040 A1	Pulse-signaling circuits for networks on chip	20070524 327/291
US 20070113053 A1	MULTITHREADING INSTRUCTION SCHEDULER EMPLOYING THREAD GROUP PRIORITI	20070517 712/214
US 20070106990 A1	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-INDE	20070510 718/102
US 20070106989 A1	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-INDE	20070510 718/102
US 20070106988 A1	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-INDE	20070510 718/102
US 20070106887 A1	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-INDE	20070510 712/228
US 20070105631 A1	Video game system using pre-encoded digital audio mixing	20070510 463/42
US 20070089112 A1	BARREL-INCREMETER-BASED ROUND-ROBIN APPARATUS AND INSTRUCTION DISP/	20070419 718/100
US 20070088861 A1	Analyzing the behavior of a storage system	20070419 710/15
US 20070083813 A1	Generic, multi-instance method and GUI detection system for tracking and monitoring compute	20070412 715/709
US 20070083491 A1	Storage of key in non-volatile memory	20070412 707/3
US 20070074182 A1	SYSTEMS, METHODS AND APPARATUS FOR MODELING, SPECIFYING AND DEPLOYING	20070329 717/136
US 20070074180 A1	Systems, Methods and Apparatus for Procedure Development and Verification	20070329 717/136
US 20070074171 A1	Per thread buffering for storing profiling data	20070329 717/127
US 20070067755 A1	SYSTEMS, METHODS AND APPARATUS FOR PATTERN MATCHING IN PROCEDURE DE	20070322 717/135
US 20070044106 A2	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-INDE	20070222 718/108
US 20070044105 A2	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-INDE	20070222 718/108
US 20070043935 A2	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-INDE	20070222 712/228
US 20070038429 A1	System simulation method	20070215 703/14
US 20070019016 A1	Printer comprising a printhead and at least two printer controllers connected to a common inpu	20070125 347/13
US 20070010329 A1	Video game system using pre-encoded macro-blocks	20070111 463/42
US 20070009043 A1	Video game system using pre-encoded macro-blocks and a reference grid	20070111 375/240.24
US 20070009042 A1	Video game system using pre-encoded macro-blocks in an I-frame	20070111 375/240.24

US 20070009036 A1	Video game system having an infinite playing field	20070111 375/240.16
US 20070009035 A1	Video game system using pre-generated motion vectors	20070111 375/240.16
US 20070009029 A1	Video encoder with latency control	20070111 375/240.12
US 20070006231 A1	Mechanism for instruction set based thread execution on a plurality of instruction sequencers	20070104 718/100
US 20070006171 A1	System development tool	20070104 717/131
US 20060294347 A1	Programmable event driven yield mechanism which may activate service threads	20061228 712/244
US 20060294312 A1	Generation sequences	20061228 711/122
US 20060282839 A1	Mechanism for monitoring instruction set based thread execution on a plurality of instruction se	20061214 719/318
US 20060274112 A1	Printhead comprising different printhead modules	20061207 347/42
US 20060253271 A1	Method for facilitating transformation of multi-threaded process-oriented object code to event-t	20061109 703/15
US 20060248527 A1	Platform independent replication	20061102 718/1
US 20060236136 A1	Apparatus and method for automatic low power mode invocation in a multi-threaded processor	20061019 713/300
US 20060236135 A1	Apparatus and method for software specified power management performance using low power	20061019 713/300
US 20060230317 A1	System and method for benchmarking	20061012 714/38
US 20060221980 A1	Method and apparatus for a high efficiency two-stage rotating priority arbiter with predictable a	20061005 370/400
US 20060206692 A1	Instruction dispatch scheduler employing round-robin apparatus supporting multiple thread pri	20060914 712/215
US 20060206635 A1	DMA engine for protocol processing	20060914 710/22
US 20060195683 A1	Symmetric multiprocessor operating system for execution on non-independent lightweight thre	20060831 712/228
US 20060190946 A1	Symmetric multiprocessor operating system for execution on non-independent lightweight thre	20060824 718/108
US 20060190945 A1	Symmetric multiprocessor operating system for execution on non-independent lightweight thre	20060824 718/108
US 20060187251 A1	Printer controller for supplying data to a printhead module having interleaved shift registers	20060824 347/13
US 20060184769 A1	Localized generation of global flush requests while guaranteeing forward progress of a proces	20060817 712/216
US 20060181558 A1	Printhead module having horizontally grouped firing order	20060817 347/12
US 20060179439 A1	Leaky-bucket thread scheduler in a multithreading microprocessor	20060810 718/105
US 20060179429 A1	Building a wavecache	20060810 717/151
US 20060179284 A1	Multithreading microprocessor with optimized thread scheduler for increasing pipeline utilizatio	20060810 712/219
US 20060179283 A1	Return data selector employing barrel-incrementer-based round-robin apparatus	20060810 712/218
US 20060179281 A1	Multithreading instruction scheduler employing thread group priorities	20060810 712/214
US 20060179280 A1	Multithreading processor including thread scheduler based on instruction stall likelihood predic	20060810 712/214
US 20060179279 A1	Bifurcated thread scheduler in a multithreading microprocessor	20060810 712/214
US 20060179276 A1	Fetch director employing barrel-incrementer-based round-robin apparatus for use in multithrea	20060810 712/205
US 20060179274 A1	Instruction/skid buffers in a multithreading microprocessor	20060810 712/205
US 20060179207 A1	Processor instruction retry recovery	20060810 711/100
US 20060179194 A1	Barrel-incrementer-based round-robin apparatus and instruction dispatch scheduler employing	20060810 710/111
US 20060168399 A1	Automatic generation of software-controlled caching and ordered synchronization	20060727 711/118
US 20060164462 A1	Print controller for supplying data to a printhead comprising different printhead modules	20060727 347/40
US 20060164455 A1	Printhead module having operation controllable on basis of thermal sensors	20060727 347/14
US 20060164454 A1	Method for at least partially compensating for errors in ink dot placement due to erroneous roto	20060727 347/14
US 20060164453 A1	Printhead module having nozzle redundancy	20060727 347/13
US 20060164452 A1	Printer controller for supplying data to a printhead capable of printing a maximum of n channel	20060727 347/12
US 20060164451 A1	Method of expelling ink from nozzles in groups, alternately, starting at outside nozzles of each	20060727 347/12
US 20060161921 A1	Preemptive multitasking employing software emulation of directed exceptions in a multithreadi	20060720 718/102
US 20060161421 A1	Software emulation of directed exceptions in a multithreading processor	20060720 703/26
US 20060150183 A1	Mechanism to emulate user-level multithreading on an OS-sequestered sequencer	20060706 718/100
US 20060143454 A1	Storage of multiple keys in memory	20060629 713/170
US 20060143350 A1	Apparatus, method and system for aggregating computing resources	20060629 710/242
US 20060139681 A1	Use of variant and base keys with three or more entities	20060629 358/1.14
US 20060139388 A1	Printer controller for supplying dot data to at least one printhead module having faulty nozzle	20060629 347/13
US 20060139387 A1	Printer controller for providing data and command via communication output	20060629 347/13

US 20060139386 A1	Printhead module having nozzle redundancy for faulty nozzle tolerance	20060629 347/12
US 20060139380 A1	Printer controller for causing expulsion of ink from nozzles in groups, starting at outside nozzle	20060629 347/5
US 20060136725 A1	Use of variant and base keys with two entities	20060622 713/171
US 20060132822 A1	Storage of program code in arbitrary locations in memory	20060622 358/1.14
US 20060132525 A1	Printer controller for at least partially compensating for erroneous rotational displacement	20060622 347/19
US 20060132521 A1	Printer controller for controlling a printhead with horizontally grouped firing order	20060622 347/14
US 20060132518 A1	Printhead module having interleaved shift registers	20060622 347/12
US 20060132516 A1	Printer controller for causing expulsion of ink from nozzles in groups, alternately, starting at our	20060622 347/9
US 20060132512 A1	Printhead module capable of printing a maximum of n channels of print data	20060622 347/5
US 20060130010 A1	Model checking with bounded context switches	20060615 717/136
US 20060129906 A1	Component models	20060615 715/500
US 20060129806 A1	Key transportation	20060615 713/161
US 20060125876 A1	Printer comprising two uneven printhead modules and at least two printer controllers, one of w	20060615 347/40
US 20060125863 A1	Printer controller for controlling a printhead module based on thermal sensing	20060615 347/14
US 20060125861 A1	Printer comprising two printhead modules and at least two printer controllers	20060615 347/13
US 20060125859 A1	Printer controller for supplying data to a printhead module having a dropped row	20060615 347/12
US 20060125858 A1	Printer controller for supplying data to a printhead module having one or more redundant nozz	20060615 347/12
US 20060125857 A1	Printhead module having a communication input for data and control	20060615 347/12
US 20060125855 A1	Printer controller for supplying data to one or more printheads via serial links	20060615 347/9
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US 20060110199 A1	PRINTER COMPRISING TWO UNEVEN PRINTHEAD MODULES AND AT LEAST TWO PRII	20060525 400/62
US 20060100845 A1	Multiple stream real time data simulation adapted for a KStore data structure	20060511 703/22
US 20060098044 A1	Printhead module having a dropped row	20060511 347/43
US 20060098042 A1	Method of manufacturing left-handed and right-handed printhead modules	20060511 347/40
US 20060095606 A1	Method, system and storage medium for lockless InfiniBand™ Poll for I/O completion	20060504 710/46
US 20060092222 A1	Printhead module for expelling ink from nozzles in groups, alternately, starting at outside nozzl	20060504 347/43
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US 20060072952 A1	Printhead formed with offset printhead modules	20060406 400/62
US 20060067592 A1	Configurable image processor	20060330 382/303
US 20060061795 A1	Storage of key in arbitrary locations in memory	20060323 358/1.14
US 20060047873 A1	Scalable, two-stage round robin arbiter with re-circulation and bounded latency	20060302 710/243
US 20060013286 A1	Configurable multimode despreader for spread spectrum applications	20060119 375/141
US 20060002412 A1	Methods and apparatus for supporting programmable burst management schemes on pipeline	20060105 370/412
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US 20050235075 A1	Method of and device for changing an output rate	20051020 710/52
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US 20050204348 A1	Software self-defense systems and methods	20050915 717/140
US 20050204316 A1	Predictable design of low power systems by pre-implementation estimation and optimization	20050915 716/2
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US 20040163083 A1	Programmable event driven yield mechanism which may activate other threads	20040819 718/102
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US 20010040915 A1	Configurable multimode despreader for spread spectrum applications	20011115 375/150
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US 7275805 B2	Printhead comprising different printhead modules	20071002 347/42
US 7267417 B2	Printer controller for supplying data to one or more printheads via serial links	20070911 347/13
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US 7188928 B2	Printer comprising two uneven printhead modules and at least two printer controllers, one of which is a printhead	20070313 347/40
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US 6332178 B1	Method for estimating statistics of properties of memory system transactions	20011218 711/118
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